

**Figure 7** Straight-to-curved transition loss variation as a function of  $X_s/Z_s$  for sine S-bends for  $X_s = 250 \ \mu \text{m}$ ,  $\theta_B = 0.4^\circ$ ,  $0.5^\circ$ ,  $0.6^\circ$ , and  $\lambda = 1.55 \ \mu \text{m}$ 

The loss values have been calculated for a number of assorted values of  $X_s$  and  $Z_s$ . As can be seen, these loss values are negligibly small. For very low-loss integrated-optic waveguides, such as those reported in [14] with propagation loss values of 0.02 dB/cm, such loss improvements can be meaningful.

## **IV. CONCLUSION**

The new slope-matched sine/cosine S-bend designs presented here show a performance better than that of the conventional sine/cosine S-bends when used for connecting offset straight waveguides inclined with respect to each other; the performance remains better for a wide range of longitudinal and transverse offset parameters. The loss performance of the slope-matched designs can be improved further by incorporating lateral offset between the straight and the curved waveguides.

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TABLE 2 Calculated Values of Optimum Lateral Offset  $a_{opt}$ and the Values of Straight-to-Curve Transition Loss, with and without Lateral Offset, for Cosine S-Bends;  $\theta_B = 0.5^\circ$ ,  $x_B = 10 \mu$ m,  $z_B = 1146 \mu$ m, and  $\lambda = 1.55 \mu$ m

X <sub>s</sub> , Z <sub>s</sub> (μm)	L <sub>sc</sub> (Without Offset) (dB)	a <sub>opt</sub> (μm)	$\begin{array}{c} L_{sc} \left( a_{\text{opt}} \right) \\ \text{(With Offset)} \\ \text{(dB)} \end{array}$
125, 12,000	5.3 <i>e</i> -05	0.02	2.9 <i>e</i> -10
125,9000	3.0 <i>e</i> -04	0.04	1.0 <i>e</i> -08
125, 4500	1.4 <i>e</i> -02	0.29	2.3 <i>e</i> -05
125, 4000	2.8 <i>e</i> -02	0.42	9.0 <i>e</i> -05
125, 3000	1.7e-01	1.0	3.2 <i>e</i> -03
125, 2500	5.8 <i>e</i> -01	1.9	4.0 <i>e</i> -02
250, 24,000	1.2 <i>e</i> -05	8.5 <i>e</i> -03	7.5 <i>e</i> -12
250, 18,000	5.9 <i>e</i> -05	0.15	3.7e-10
250, 9000	1.9 <i>e</i> -03	0.31	4.5 <i>e</i> -07
250, 8000	3.6 <i>e</i> -03	0.49	1.5 <i>e</i> -06

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# DESIGN OF THE SMART OPTOELECTRONIC NOT GATE BASED ON DIRECTIONAL COUPLERS

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ABSTRACT: A novel smart optoelectronic NOT gate has been proposed. It takes advantage of the linear device operation and the easy-toimplement logic function as compared to conventional optoelectronic logic gates. Detailed investigations of the configuration of the NOT gate, optoelectronic converters, and numerical analysis have been considered. © 2000 John Wiley & Sons, Inc. Microwave Opt Technol Lett 24: 271–275, 2000.

**Key words:** *optoelectronic; optical waveguide; logic gate; directional coupler* 

#### I. INTRODUCTION

Optical communication systems have attracted many researchers and have stimulated great developments in highspeed optoelectronic logic gates in recent years. Ultrafast optoelectronic logic gates also will be indispensable in optical communication systems in the near future. Generally, the most commonly used techniques for optoelectronic logic gates were nonlinear optical devices. Although nonlinear operations have been implemented by several methods, these methods still present some difficulties for practical applications.

Following the self-pumped phase conjugate mirror (SP-PCM) technique, much research that focused on all-optical logic operations in photorefractive materials such as  $BaTiO_3$  crystals has been successfully completed [1–4]. Qiu et al. demonstrated OR, NOT, and NOR optical logic gates by using the induced and erasable SPPCM in photorefractive materials [2].

To our knowledge, bistable devices can be employed as logic gates or flip-flops [5, pp. 546-563]. The bistable device adopted in optoelectronic switching is called the self-electrooptic effect device (SEED). In practical circuit design, we can employ a SEED accompanied by an electronic or electro-optic device to construct a feedback loop. For instance, a SEED can be connected in series with a resistor (R-SEED), with a field-effect transistor (F-SEED), with a heterostructure phototransistor (H-SEED), or with another SEED (symmetric SEED or S-SEED). For most logical operations, multiple SEEDs can be integrated together, and are called logic-SEED (L-SEED). In [6], Ohno et al. implemented a new optical inverter, which is composed of two resonant tunneling transistors, and the logic function is realized by using the monostable-bistable transition of the logic gate. Finally, Hall and Rauschenbach first demonstrated AND and NOT functionalities with a clock stream and a data stream as the logic inputs to an SOA-based switch for the ultrafast nonlinear interferometer [7].

In summary, the key problem of the traditional optoelectronic logic gates is the limitation in nonlinear operation. As a result, it is difficult for practical application in optical computing systems. Hence, the best way to improve the nonlinear operation is to employ devices with a linear function, such as Ti: LiNbO<sub>3</sub> directional coupler switches.

This paper is organized as follows. Section II describes the configuration of the smart optoelectronic NOT gate. Section III shows the most essential part of the smart optoelectronic logic, i.e., the directional coupler, describing its architecture and characteristics. Section IV depicts the characteristics of the different optoelectronic (OE) converters. Section V illustrates the characteristics of the directional couplers, such as the switching voltage, device parameters, and crosstalk by means of numerical analysis, and discusses the smart optoelectronic NOT gate. Finally, Section VII is the conclusion.

## **II. SMART OPTOELECTRONIC NOT GATE CONFIGUATIONS**

The smart optoelectronic NOT gate uses electro-optic switches to realize a logic algorithm. The major configurations are composed of directional coupler switches, optoelectronic converters, EDFAs, 1:1 optical couplers, and delay lines. The directional couplers and optoelectronic converters are illustrated in the third and fourth section, respectively. The EDFAs are used to achieve identical input of the power level. In addition, the purpose of the delay lines is to synchronize the optical and electrical signal in the directional goupler.

The circuit configuration and truth table of the smart optoelectronic NOT gate are clearly described as follows. The inverting theorem is implemented by the NOT gate in a  $1 \times 1$  configuration, as shown in Figure 1(a). The NOT operation is that input A is a complement of output X. In other words, if A = 1, then X = 0; or if A = 0, then X = 1, as described in the truth table of Figure 1(b).

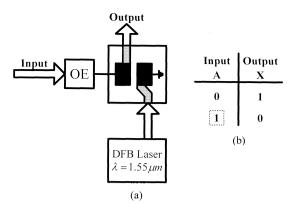
#### **III. DIRECTIONAL COUPLER**

The most commonly used materials in integrated optical devices can be categorized into three kinds: glass waveguide, III–V compound semiconductors such as GaAs and InP, and isolated ferroelectric crystals such as LiNbO<sub>3</sub> and LiTaO<sub>3</sub>. Among those materials, LiNbO<sub>3</sub> has good characteristics of electro-optic (EO) and acousto-optic (AO) modulation.

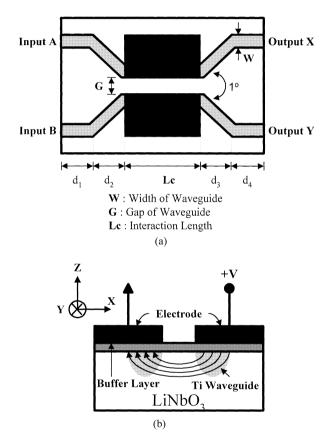
A. Architecture of Directional Couplers. Because LiNbO<sub>3</sub> has many advantages, such as low waveguide loss and high electro-optic coefficients, it is one of the most important materials in integrated optical devices. Among LiNbO3 waveguide devices, titanium-indiffused LiNbO<sub>2</sub> (Ti: LiNbO<sub>2</sub>) is the most mature and well-developed technology. Therefore, a Ti: LiNbO<sub>3</sub> directional coupler is a good choice for the design of smart photonic logic gates. The method to implement a directional coupler for z-cut Ti: LiNbO<sub>3</sub> is described as follows [8]. First, a directional coupler with uniform coupling is designed and fabricated, and the switching voltage is increased due to the coupling constant weighting effect. The second method is employed by the uniform  $\Delta\beta$  driving technique, presenting a cross state under the condition of no bias. In the third, selecting titanium thickness controls the polarization state. In the final technique, the electrodes for alternating  $\Delta\beta$  were placed on the waveguides [9]; its advantage is to reduce loss and crosstalk. A pair of electrode architectures with a special design is called the COBRA (commuter optique binaire rapide) [10]. In general, the directional coupler with two pairs of electrodes using the alternating  $\Delta\beta$  technique is called reversed  $\Delta\beta$  [11, 12].

Figure 2(a) is a  $2 \times 2$  *z*-cut Ti: LiNbO<sub>3</sub> directional coupler. Because the architecture is symmetric, there is no difference in the optical path. As we know, it is very important to maintain the synchronization and obtain zero optical path difference in the layout of these smart optoelectronic NOT gates. However, in the actual application and design, the problem of optical path differences should be considered. Hence, the key problem with the polarization rotation of the optical mode can be easily solved. In addition, the electrooptic effect would be influenced by the electric field distribution in the LiNbO<sub>3</sub> waveguide, so it is also important to arrange the electrode and choose the crystal orientation. Figure 2(b) shows the arrangement of the electrode and waveguide. Several important parameters of the directional coupler, such as the switching voltage  $(V_{\pi})$ , the coupling coefficient ( $\kappa$ ), the electrode gap (G), and the interaction length  $(L_c)$ , should be discussed. The analysis of these device parameters will be described in Section V.

*B. Analysis of Directional Couplers.* The directional coupler is the most essential part of the smart optoelectronic NOT gate. Thus, analysis of the directional coupler is very important. The analysis of the focal point includes low driving voltages,



**Figure 1** Proposed smart photonic logic NOT gate. (a) Circuit configuration composed of an integrated optical directional coupler and an optoelectronic device. (b) Truth table. Note that the dashed box corresponds to the applied voltage on the directional coupler



**Figure 2** architecture of  $2 \times 2$  directional coupler. (a) Top view. (b) Cross-section

ultra-high switching frequency, low loss, and low crosstalk. It is expected that the driving voltage of the electro-optic switches is less than 5 V [13]. In [5, pp. 541–543], the switching voltage is considered in the configuration of a COBRA. If  $\delta = \sqrt{3} \kappa$ , then the condition of the switch is in the bar state. The required switching voltage is

$$V_{\pi} = \frac{\sqrt{3}\,\lambda}{2\gamma L_c} \tag{1}$$

and

$$\gamma = \frac{m^3}{G}, \, L_c = \frac{\pi}{2\kappa}$$

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where  $\lambda$  is the wavelength of laser light, *r* is the electro-optic coefficient, *n* is the refractive index, *G* is the gap,  $\kappa$  is the coupling coefficient,  $L_c$  is the interaction length, and  $\gamma$  is an effective parameter representing the effect of electro-optic modulation. Nevertheless, the refractive index is relative to the laser wavelength with a TE and TM mode. For example, at a wavelength of  $\lambda = 1.55 \ \mu\text{m}$ , if  $\kappa = 0.71 \times 10^{-3} \ 1/\text{m}$ , then  $L_c = 2.21 \ \text{mm}$  can be determined. If  $G = 1 \ \mu\text{m}$ ,  $r_{33} = 30.8 \times 10^{-12} \ \text{m/V}$ , and  $n_{\text{TM}} = 2.144$ , we can obtain  $\gamma_{\text{TM}} = 3.04 \times 10^{-4} \ 1/\text{V}$  and  $V\pi = 2 \ \text{V}$ .

#### **IV. OPTOELECTRONIC CONVERTERS**

The basic principle of the optoelectronic converter is that a photodiode is used to absorb photons and generate a strong electric field to excite more electron-hole pairs, resulting in a larger photocurrent. These characteristics are satisfied with the smart optoelectronic NOT gate. To match the directional coupler, the optoelectronic converter must include an impedance-matching circuit. If the output voltage is small, a FET amplifier must be used to increase the voltage. Furthermore, optoelectronic converters have many types. These optoelectronic converters based on GaAs, InGaAs, and In-GaAsP have been studied and developed. We will introduce six optoelectronic converters as follows.

A. P-I-N PD (P-Type, Intrinsic, N-Type Photodiode). The p-i-n photodetector [5, pp. 182–217, 14] has a sandwiched p-i-n semiconductor structure inserting an intrinsic (i) layer between n-type and p-type layers. The n-type absorbs incident photons, and then the photocurrent is generated in the intrinsic layer, and the p-type layers usually have relatively higher doping levels compared to the lightly doped intrinsic layer.

B. UTC PD (Unitraveling-Carrier Photodiode). The UTC PD uses the MOCVD technique in the InP substrate [15–18]. The UTC PD, with an absorption (InGaAs) layer and a carrier-collecting layer (InP), enables us to exploit only photoexcited electrons as active carriers in the p-i-n structure, and the velocity overshoot in the InP collecting layer enhances the saturation output.

*C. MSM PD (Metal–Semiconductor–Metal Photodiode).* The MSM PD is similar to the *p-i-n* PD, but the *p*-type and *n*-type semiconductors are replaced by metal contacts. These two metal contacts are interleaved in an interdigitated structure to reduce two-plate capacitance. So, the MSM PD has a lower junction capacitance to cause high sensitivity and speeds [19–22].

D. P-I-N / FET. In general, GaAs and InP (InGaAs and AlGaAs) are good materials for FETs (*p-i-n* diodes). However, the sensitivity of a *p-i-n*/FET only has about 100 MHz for the limited transconductance gain from the FET, and it is not as good as the best hybrid circuits [23, 24].

*E. HPT.* The heterophototransistor (HPT) [25] has a base (p-InGaAs) and collector (n-InGaAs) function as a photo-

diode at reverse bias. By adding the emitter (n-InP), it creates an *n-p-n* transistor. Because the switching speed of the transistor is dependent on the charging and discharging speed of the base-emitter capacitor, the speed of a photo-transistor is determined by the incident light power, which generates the current to charge the base-emitter capacitor. Therefore, it can be used for high-speed and high-sensitivity communications.

F. P-I-N / HBT. The heterojunction bipolar transistors (HBTs) are very important for the applications of ultra-fastspeed logic for microwave power devices [26]. The HBT technology with the conventional multimedia process has two limitations: First, the device topography presents step heights of larger than 1  $\mu$ m for limitation in large-scale integration. The second limitation is the impossibility of simultaneously decreasing the base transit time and the base resistance for a tradeoff between the base layer thickness and doping concentration. The planar processes with ion implantation or epitaxial regrowth techniques can improve the device performance, the reliability of the metal connections, and the scale integration. These processes would optimize the HBT performances with a thin intrinsic base for a low base transit time and a thick and highly doped extrinsic base for low base resistance [2].

#### V. NUMERICAL ANALYSIS OF DIRECTIONAL COUPLER

The determination of the gap (G), the interaction length  $(L_c)$ , and the coupling coefficient  $(\kappa)$  of the directional coupler is dependent on the operating wavelength  $(\lambda)$ , TE or TM modes, switching voltages, switch frequency, and crosstalk.

A. Switching Voltage. From Eq. (1), we can obtain the switching voltage of the directional coupler versus the gap at operating wavelengths ( $\lambda$ ) of 1.3 and 1.55  $\mu$ m. The switching voltage of the TE mode is larger than that of the TM mode. As  $\kappa$  increases, the switching voltage increases. Taking  $\kappa =$  $1.0 \times 10^3$  1/m and a switching voltage of 10 V as an example, we obtain gaps of 1.34 (1.63) and 3.8  $\mu$ m (4.56  $\mu$ m) for TE and TM modes, respectively, under an operating wavelength of 1.55  $\mu$ m (1.3  $\mu$ m). Given the same value of G, we can use the TM mode at the lower switching voltage. That is, the TM mode switch is a better choice for considering the lower operating voltage.

*B. Electrode Gap.* We estimate the required gap and coupling coefficient for switching voltages of 2 and 5 V. As the gap increases, the switching voltage increases. Taking  $\kappa = 1.0 \times 10^3 \text{ 1/m}$  and a switching voltage of 2 V as an example, we obtain gaps of 0.26 (0.32) and 0.8  $\mu$ m (0.9  $\mu$ m) for TE and TM modes, respectively, under an operating wavelength of 1.55  $\mu$ m (1.3  $\mu$ m). For a larger switching voltage of 5 V, the gaps are 0.7  $\mu$ m (0.8) and 1.9  $\mu$ m (2.3  $\mu$ m) for TE and TM modes, respectively, under an operating wavelength of 1.55  $\mu$ m (1.3  $\mu$ m). It is found that the required gap for the photonic switch is smaller at a wavelength of 1.55  $\mu$ m.

*C. Dielectric Loss.* From Eqs. (2) and (3), we obtain the dielectric and propagation loss versus the operating frequency. As the operating frequency increases, the dielectric and propagation loss increases. For operating frequencies of

20 and 100 GHz, we can calculate total losses of 1.48 and  $\frac{3}{8}$  4 dB/cm, respectively.

D. Crosstalk. From Eq. (1) and without consideration of crosstalk, we can obtain the gap versus the interaction length  $(L_c)$  for applied voltages of 2 and 5 V together with different wavelengths of 1.3 and 1.55  $\mu$ m. Taking an interaction length of 10 mm and an operating wavelength of 1.55  $\mu$ m, the required gaps are 12.3 and 4.96  $\mu$ m for switching voltages of 5 and 2 V, respectively, under the TM mode. When the gap width is 4  $\mu$ m, consider the crosstalk to be -20 dB [10]; we need to increase the interaction length from 2.67 (6.67) to 7.84 mm (19.6 mm) for a switching voltage of 5 V (2 V) and an operating wavelength of 1.3  $\mu$ m. For an operating wavelength of 1.55  $\mu$ m, the interaction length is not influenced by the crosstalk, and increases from 3.25 to 8.13 mm as the switching voltage decreases from 5 to 2 V. Therefore, an operating wavelength of 1.55  $\mu$ m is the better solution when the crosstalk is considered.

### **VI. CONCLUSIONS**

The main components of these optoelectronic NOT gates are the integrated optical directional couplers, optoelectronic converters, erbium-doped fiber amplifiers (EDFAs), 1:1 optical couplers, and delay lines. The characteristics of the optoelectronic NOT gate, such as the switching voltage, device parameters, and crosstalk, have been investigated and proved to be informative. The gap (G), the interaction length  $(L_c)$ , and the coupling coefficient ( $\kappa$ ) of the directional coupler can be determined by the operating wavelength, TE or TM modes, switching voltages, and crosstalk. It has been found that the TM-mode optoelectronic switch with an operating wavelength of 1.55  $\mu$ m is better for lower power and highspeed switches. Under the condition of a TM mode,  $L_c =$ 10 mm, and  $\lambda = 1.55 \ \mu$ m, the required gaps are 12.3 and 4.96  $\mu$ m for switching voltages of 5 and 2 V, respectively. In the near future, it is believed that this will be of great interest in the design of smart optoelectronic systems.

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# ELECTROMAGNETIC WAVE SCATTERING FROM 2-D CYLINDER BY USING THE METHOD OF LINES

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ABSTRACT: The method of lines is used to solve electromagnetic wave scattering from a cylinder with arbitrary shapes. The computational efficiency and accuracy are much better than finite difference method and finite element method. © 2000 John Wiley & Sons, Inc. Microwave Opt Technol Lett 24: 275–277, 2000.

**Key words:** *electromagnetic wave scattering; method of lines; wave propagation* 

#### I. INTRODUCTION

To solve scattering problems, many numerical methods have been employed, such as finite-difference methods (FDM) [1], finite-element methods (FEM) [2, 5, 6], the boundary element method (BEM) [3], the method of moments (MoM), and so on. It has been demonstrated that these numerical methods are very suitable and flexible in solving electromagnetic (EM) wave scattering (see, e.g., [2, 5, 6] and references therein). However, certain difficulties arise in the application of these methods. For example, artificial boundary conditions (ABCs) must be used to truncate the computation domains for FDM and FEM, which will result in truncating errors, and the ABCs must be relatively far from the scatterers. For BEM, no ABCs are needed, but the Green's function has to be used; thus, a special treatment is necessary for the singularities.

The method of lines (MoL) is a semianalytical method, and has been applied to solve many EM problems [7–10]. Instead of using the original MoL in Cartesian coordinates, modified MoLs (MMoLs) in the cylindrical coordinates [8–10] are used to solve the scattering from conducting cylinders without using any ABCs. Also, the MMoL is semianalytical. For validation, the scattering results from circular conducting cylinders using the MMoL are compared with the analytical solutions from the structures.

### II. METHOD OF LINES IN CYLINDRICAL COORDINATES (MMOL)

The general 2-D cylindrical structures are shown in Figure 1. The perfectly conducting cylinder has a general shape  $\Gamma$  under an incident electromagnetic plane wave  $\mathbf{F}_z = \mathbf{F}_o e^{jkx}$ , where  $\mathbf{F}$  represents  $E_z$  for TE waves and  $H_z$  for TM waves, respectively.

The scattering field satisfies the wave equation in the cylindrical coordinates [4]:

$$\left(\frac{\partial^2}{\partial r^2} + \frac{1}{r}\frac{\partial}{\partial r} + \frac{1}{r^2}\frac{\partial^2}{\partial \theta^2} + k^2\right)F(r,\theta) = 0.$$
(1)

Discretizing the whole region in the angular direction with the step  $\Delta \theta$  as shown in Figure 1, Eq. (1) becomes a set of coupled ordinary differential equations with respect to the